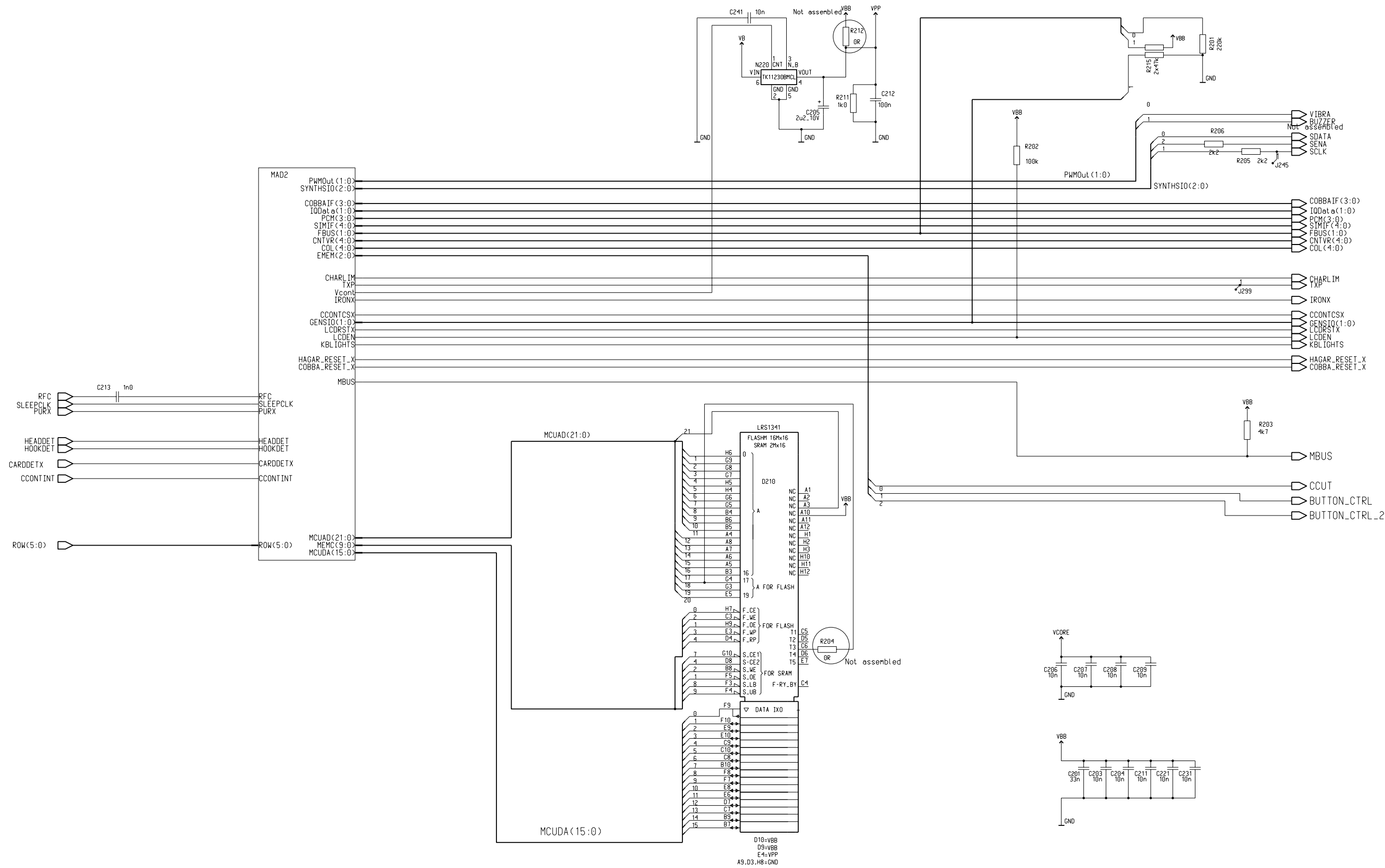
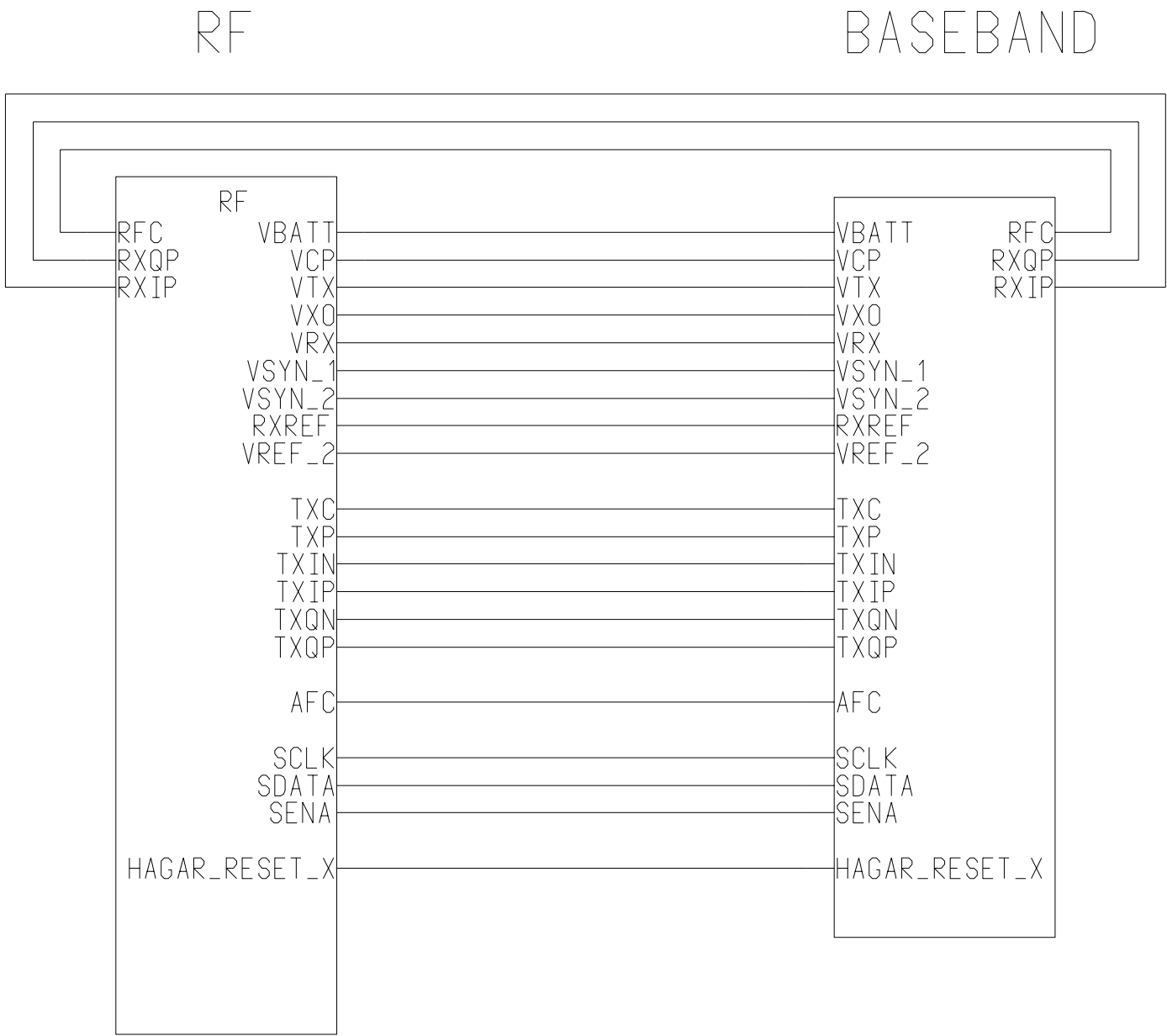


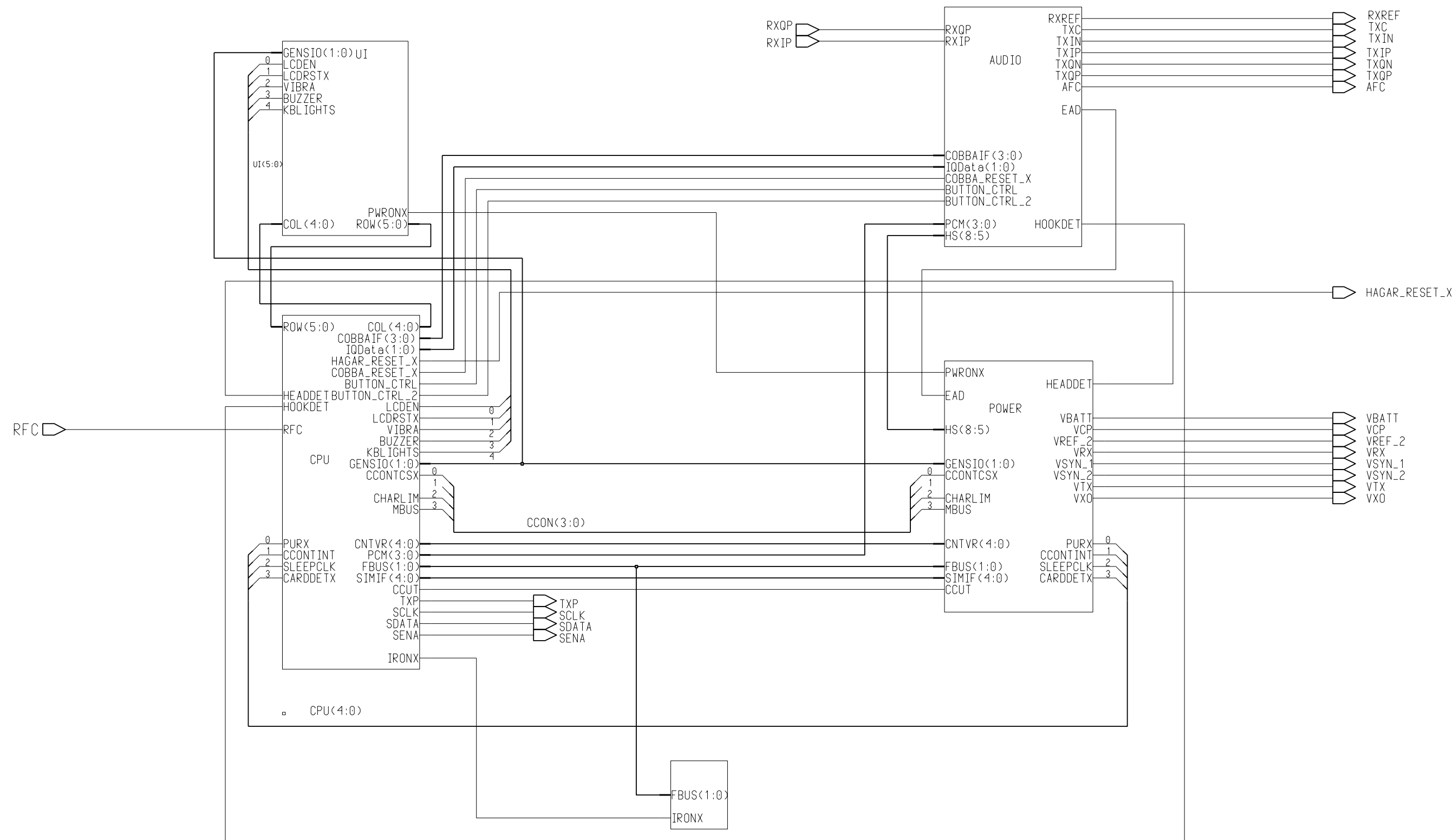
Circuit Diagram of CPU Block (45)



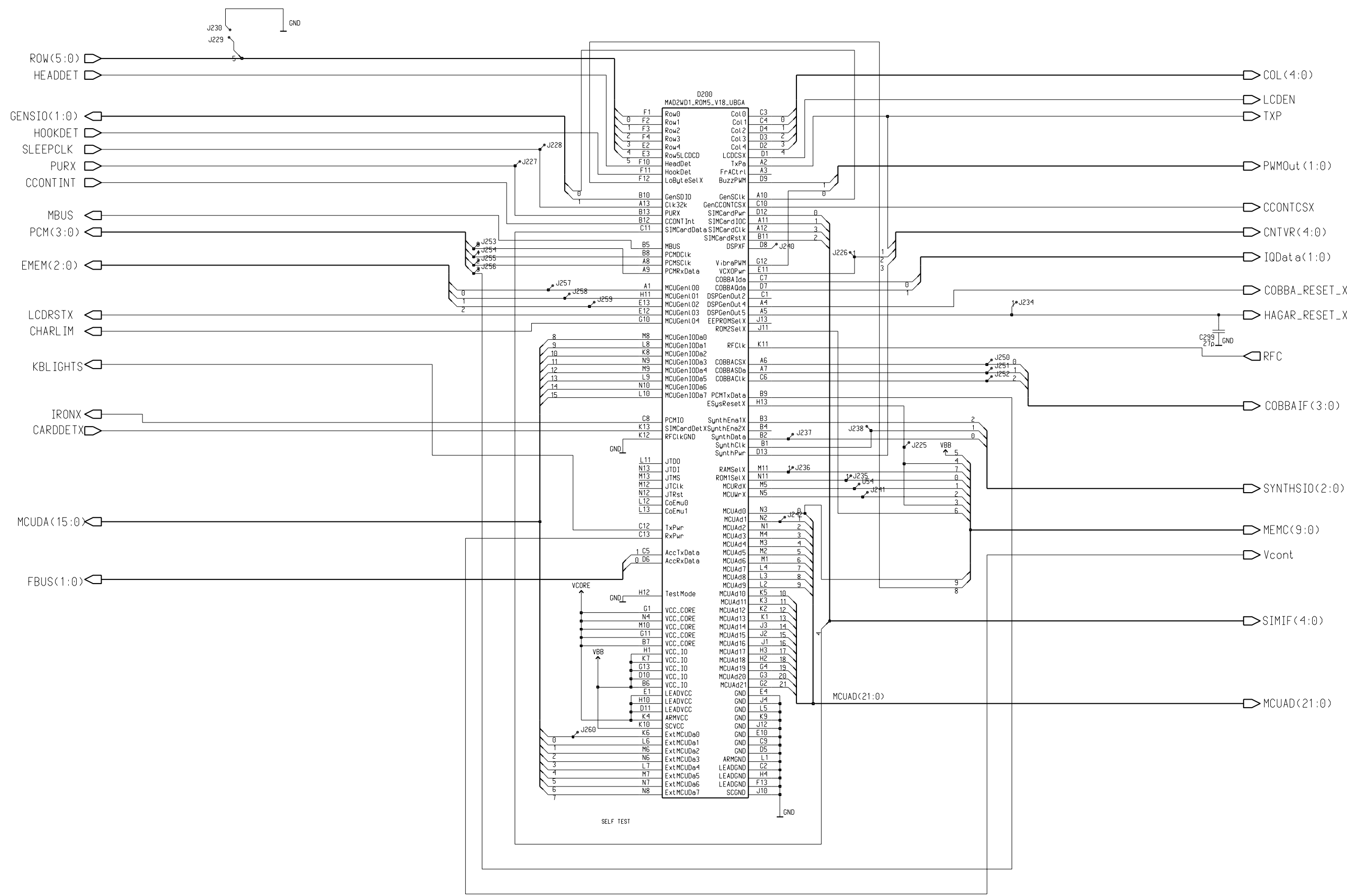
Connection between RF and Baseband modules (Edit 37)



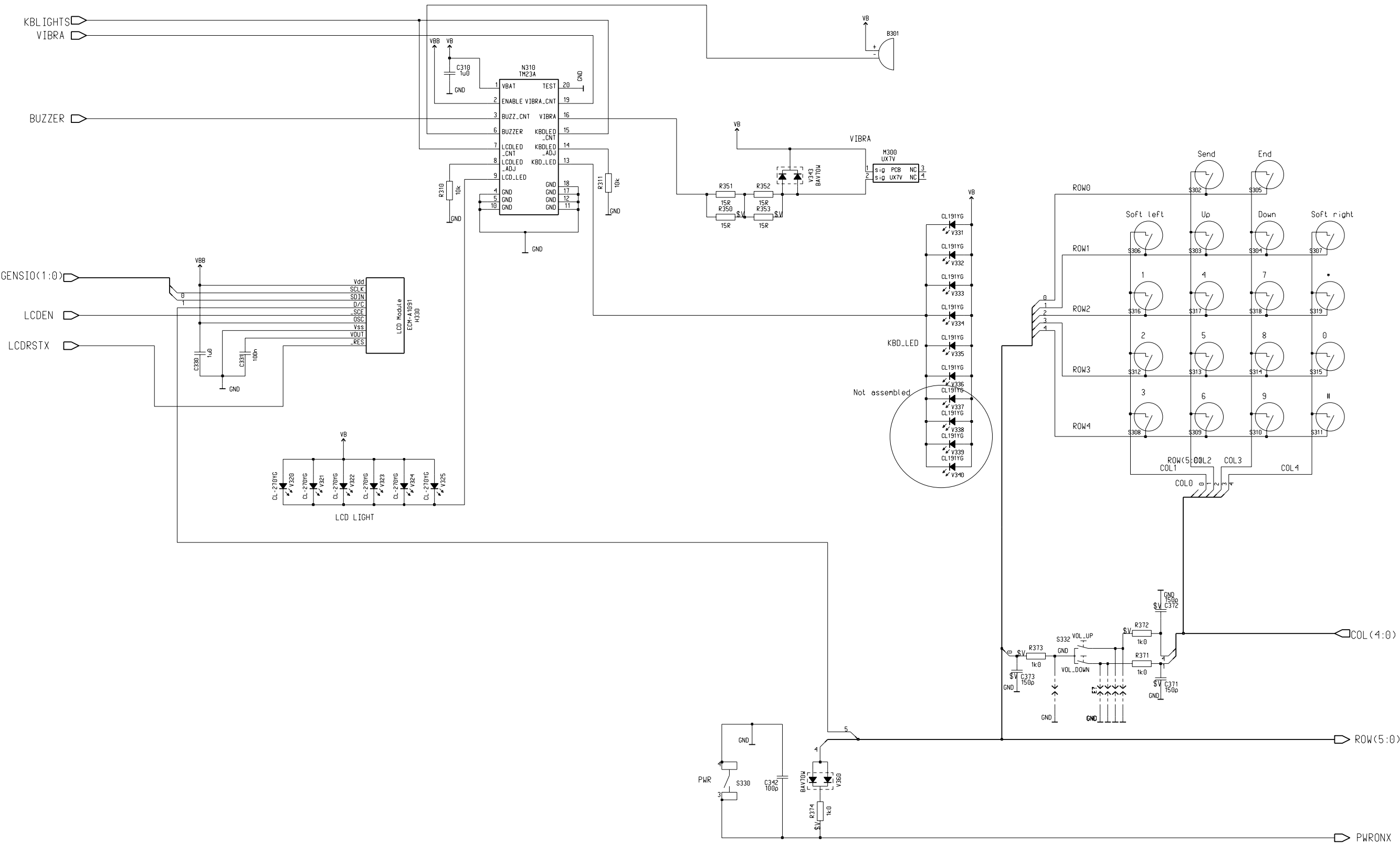
Baseband Block Interconnections (Edit 40)



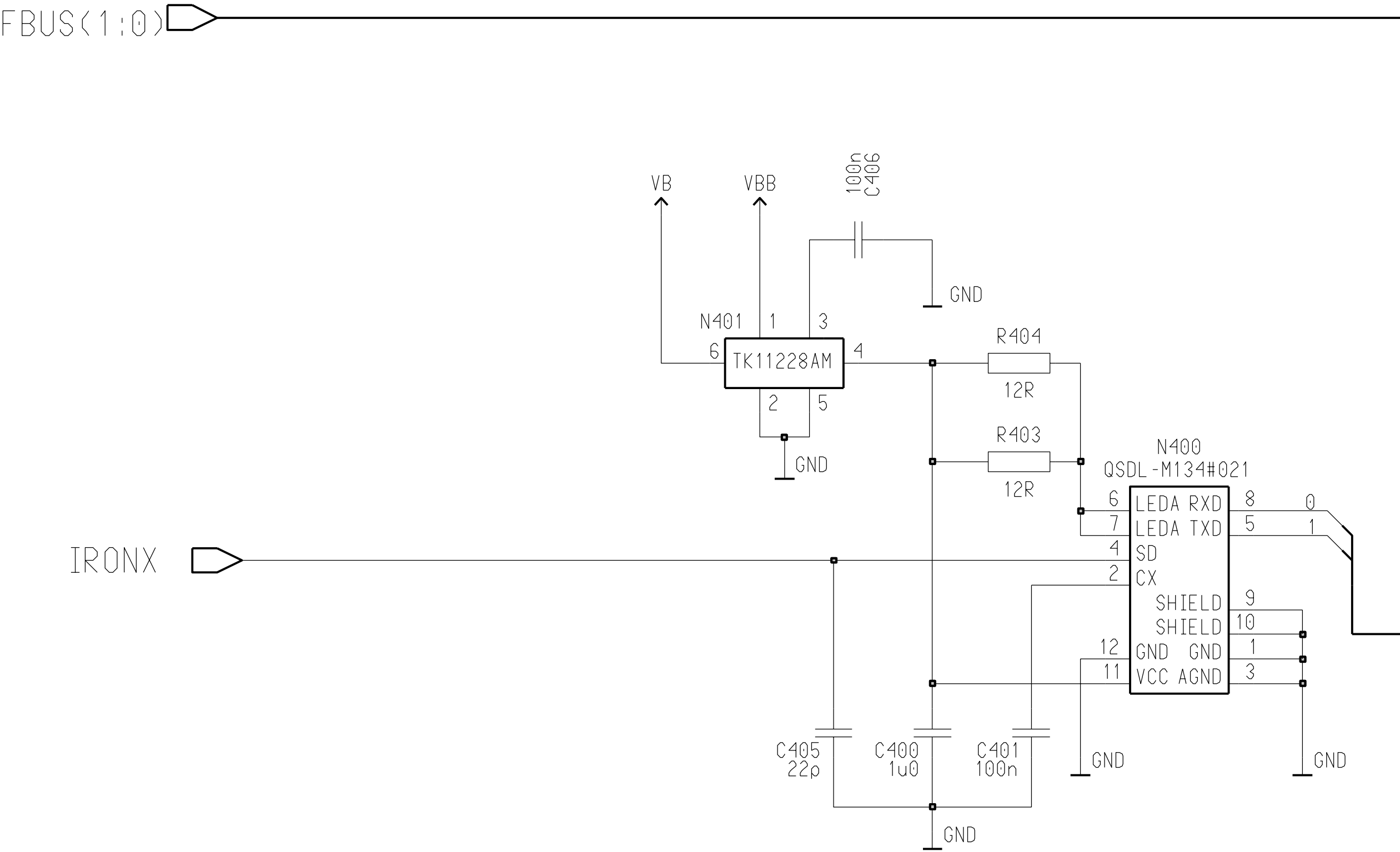
Circuit Diagram of MAD Block (Edit 27)



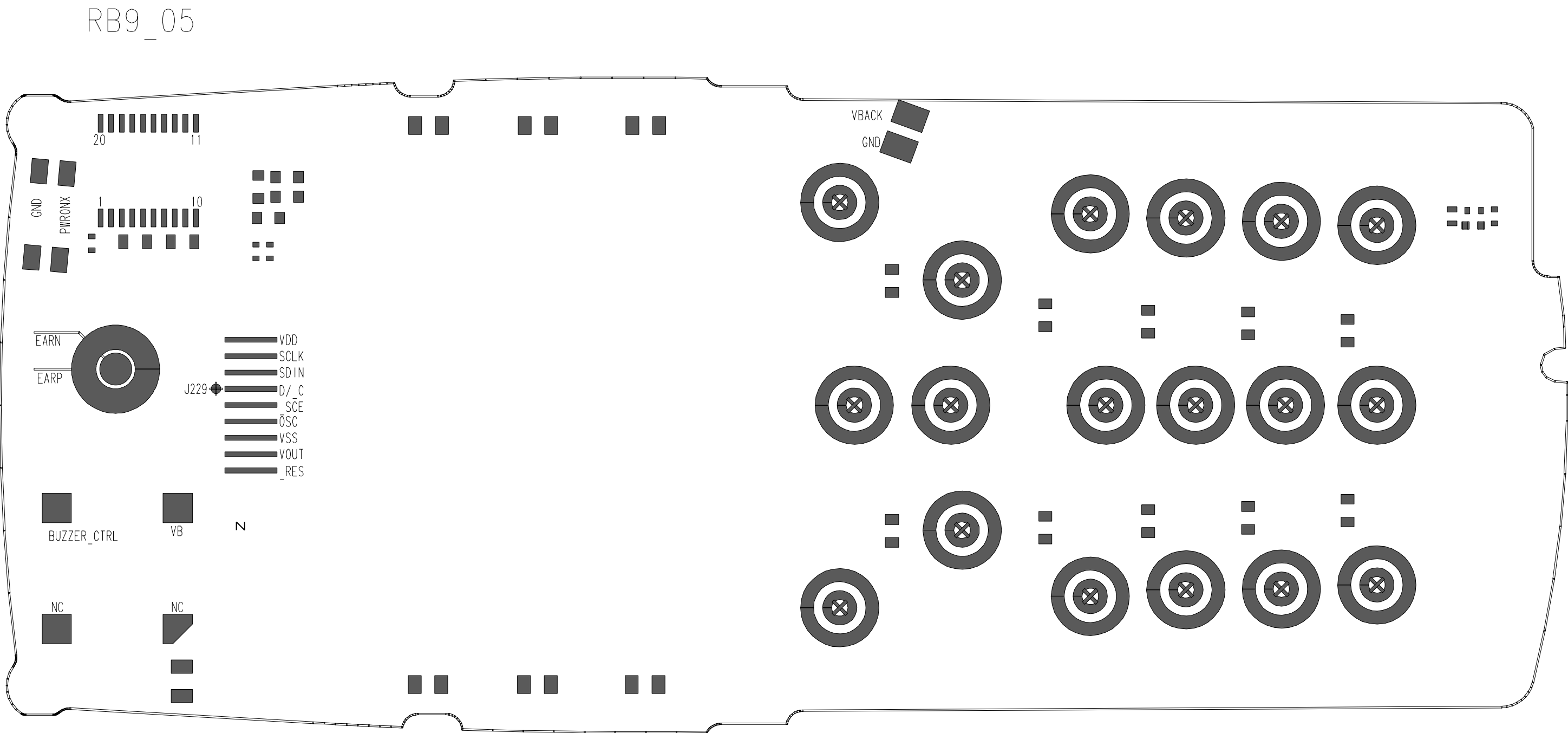
Circuit Diagram of UIF (Edit 36)



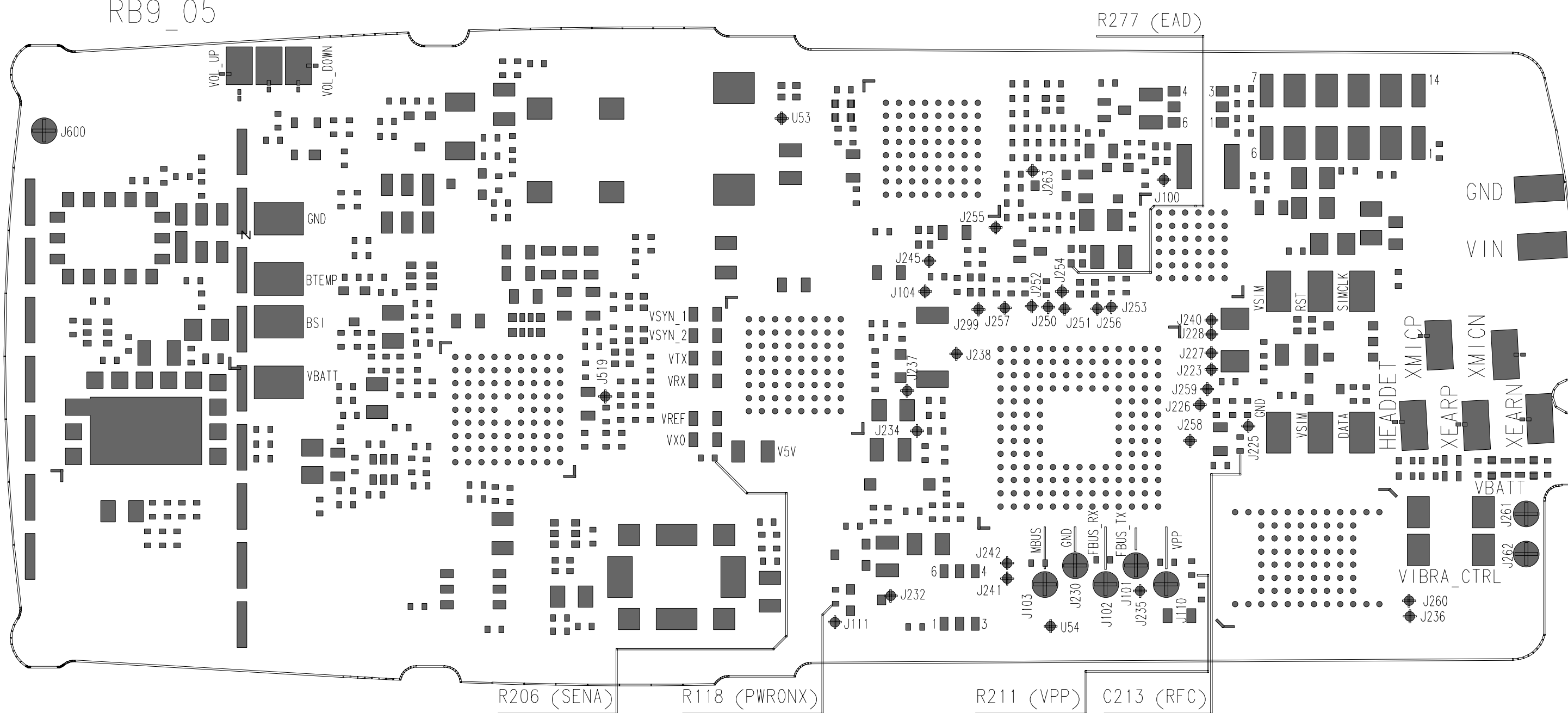
Circuit Diagram of IR Module (Edit 23)



Test Points for RB9- TOP



RB9 05



RF Testpoints for RB9

RB9_05

